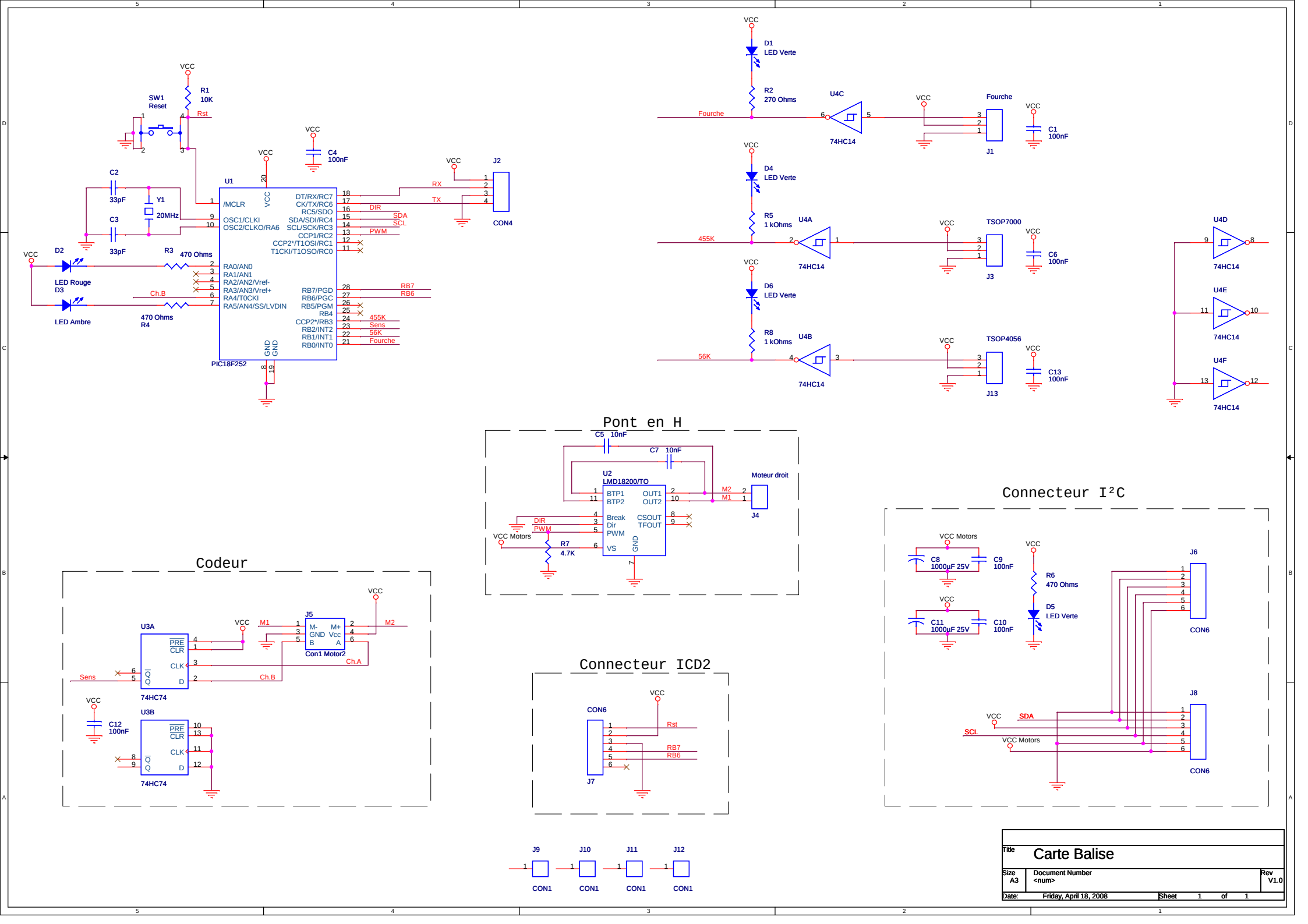
VCC
R1
10K
SW1
Reset
Rst
C4
100nF
C2
33pF
Y1
20MHz
C3
33pF
U1
/MCLR
OSC1/CLKI
OSC2/CLKO/RA6
RA0/AN0
RA1/AN1
RA2/AN2/Vref-
RA3/AN3/Vref+
RA4/T0CKI
RA5/AN4/SS/LVDIN
DT/RX/RC7
CK/TX/RC6
RC5/SDO
SDA/SDI/RC4
SCL/SCK/RC3
CCP1/RC2
CCP2*/T1OSI/RC1
T1CKI/T1OSO/RC0
RB7/PGD
RB6/PGC
RB5/PGM
RB4
CCP2*/RB3
RB2/INT2
RB1/INT1
RB0/INT0
GND
PIC18F252
J2
CON4
RX
TX
DIR
SDA
SCL
PWM
RB7
RB6
455K
Sens
56K
Fourche
D2
LED Rouge
D3
LED Ambre
R3
470 Ohms
R4
470 Ohms
Ch.B
D1
LED Verte
R2
270 Ohms
U4C
74HC14
J1
C1
100nF
D4
LED Verte
R5
1 kOhms
U4A
TSOP7000
J3
C6
100nF
D6
LED Verte
R8
1 kOhms
U4B
TSOP4056
J13
C13
100nF
U4D
U4E
U4F
Pont en H
C5 10nF
C7 10nF
U2
LMD18200/TO
BTP1
BTP2
Break
Dir
PWM
VS
OUT1
OUT2
CSOUT
TFOUT
GND
M2
M1
Moteur droit
J4
VCC Motors
R7
4.7K
Codeur
U3A
74HC74
PRE
CLR
CLK
D
Q
J5
Con1 Motor2
M-
GND
B
M+
Vcc
A
Ch.A
U3B
C12
100nF
Connecteur I²C
C8
1000µF 25V
C9
100nF
C11
1000µF 25V
C10
100nF
R6
470 Ohms
D5
LED Verte
J6
CON6
J8
SDA
SCL
Connecteur ICD2
CON6
J7
J9
J10
J11
J12
CON1
Title
Carte Balise
Size
A3
Document Number
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Rev
V1.0
Date:
Friday, April 18, 2008
Sheet
1
of
1